



09/265119

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : 6,904,400 B1
Issue Date : June 7, 2005
Inventors : Maurizio Peri et al.

Docket No. : 856063.579
Date : August 30, 2005

Mail Stop Certificate of Correction
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
SEP 07 2005
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents:

A certificate of correction is respectfully requested in the above-identified patent.

The following errors have been made:

Claim 14 contains 2 errors; 1) column 20, line 62, "memory de vice" was incorrectly changed from --memory device-- in line 1 of Claim 15, as amended on June 30, 2004, 2) column 20, line 66, "sense amplified" was incorrectly changed from --sense amplifier-- in line 4 of Claim 15 as amended on June 30, 2004. Please note, due to claim canceling and renumbering, Claim 15 has become Claim 14 in the issued patent.

Claim 15, column 21, line 11 of the issued patent, "second memory sector s" was incorrectly changed from --second memory sectors-- in line 8 of Claim 16 as amended on June 30, 2004. Please note, due to claim canceling and renumbering, Claim 16 has become Claim 15 in the issued patent.

Claim 21 contains 2 errors; 1) column 22, lines 4 and 5; "byte alterability" was incorrectly changed from --byte alterability;-- in lines 3 and 4 of Claim 22 as amended on June 30, 2004, 2) column 22, line 9, "address bits" was incorrectly changed from --address bus-- in line 7 of Claim 22 as amended on June 30, 2004. Please note, due to claim canceling and renumbering, Claim 22 has become Claim 22 in the issued patent.

Attached is form PTO/SB/44, which indicates the corrections to be made, by reference to the column and line numbers in the printed patent. Because the errors were made by the Patent Office, no fee is necessary. However if they are determined to be our errors, the Director is hereby authorized to charge payment of any fees associated with this communication to Deposit Account No. 19-1090.

Respectfully submitted,
SEED Intellectual Property Law Group PLLC



Robert Iannucci
Registration No. 33,514

Enclosures:

Form PTO/SB/44
Postcard

SEED Intellectual Property Law Group PLLC
701 Fifth Avenue, Suite 6300
Seattle, Washington 98104-7092
Phone: (206) 622-4900
Fax: (206) 682-6031

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 6,904,400 B1
DATED : June 7, 2005
INVENTORS : Maurizio Peri et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Column 20.

Line 62, "memory de vice" should read as --memory device--.

Line 66, "sense amplified" should read as --sense amplifier--.

Column 21.

Line 10, "memory sector s" should read as --memory sectors--.

Column 22.

Lines 4 & 5, "byte alterability" should read as --byte alterability;--.

Line 9, "address bits" should read as --address bus--.

MAILING ADDRESS OF SENDER:

Robert Iannucci
Seed Intellectual Property Law Group PLLC
701 Fifth Avenue, Suite 6300
Seattle, Washington 98104

PATENT NO. 6,904,400 B1

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